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AUG 25 2004

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Julie Matthews

(Depositor's name)

August 25, 2004

(Signature)

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/629,109	07/29/2003	Ping-Chen Liu	X-1174 US	5908

TITLE OF INVENTION: COLUMN REDUNDANCY SCHEME FOR SERIALY PROGRAMMABLE INTEGRATED CIRCUITS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	09/07/2004
EXAMINER		ART UNIT	CLASS-SUBCLASS		
DINH, SON T		2824	365-200000		

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1 John Kubodera

2 Edel M. Young

3 Justin Liu

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Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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Justin Liu, 54,959 August 25, 2004

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COMMISSIONER FOR PATENTS
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Inventor(s): Ping-Chen Liu et al.

Assignee: **XILINX, INC.**

conf.no. 5908

Serial No.: 10/629,109

Filed: July 29, 2003

Title: Column Redundancy Scheme for Serially Programmable Integrated Circuits

Docket No.: X-1174 US

Enclosed: Return Receipt Postcard
Part B. - Fee Transmittal

Date: August 25, 2004

VIA FACSIMILE

Atty/Sec: JL/jam